What is claimed is:

1. A liquid crystal display device comprising a first substrate on which a plurality of gate lines, a plurality of drain lines and a plurality of pixel electrodes are formed, a second substrate which is arranged to face the first substrate in an opposed manner, and a liquid crystal layer disposed between the first substrate and the second substrate, wherein

the first substrate includes a pixel region on which the pixel electrodes are formed and a peripheral region which surrounds the pixel region, and

an inspection terminal is formed on the peripheral region, and a semiconductor chip for driving the liquid crystal is arranged over the inspection terminal such that the semiconductor chip is electrically insulated from the inspection terminal.

- 2. A liquid crystal display device according to claim 1, wherein the semiconductor chip is comprised of two semiconductor chips consisting of a drain driver and a gate driver, and the drain driver is arranged over the inspection terminal.
- 3. A liquid crystal display device according to claim 1, wherein the inspection terminal includes a drain-line inspection terminal and a gate-line inspection terminal.
- 4. A liquid crystal display device including a liquid crystal layer between a first substrate and a second substrate, wherein

the first substrate includes a pixel region having pixel electrodes and a peripheral region which surrounds the pixel region,

three drain-line inspection terminals and four gate-line inspection terminals are arranged in the peripheral region, and a semiconductor chip is arranged over the three drain-line inspection terminals and the four gate-line inspection terminals.

- 5. A liquid crystal display device according to claim 4, wherein the drain-line inspection terminals include a drain-line inspection terminal for red, a drain-line inspection terminal for green and a drain-line inspection terminal for blue.
- 6. A liquid crystal display device according to claim 4, wherein neighboring gate lines are connected to different gate-line inspection terminals.